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APPLICANTS Ko Miyazaki, Kokubunji, JAPAN; Kazutaka Mori, Kokubunji, JAPAN; Norio Hasegawa, Hinode, JAPAN; Tsuneo Terasawa, Ome, JAPAN; Toshihiko Tanaka, Tokyo, JAPAN;				
** CONTINUING DATA *****				
** FOREIGN APPLICATIONS ***** JAPAN 2000-308320 10/06/2000				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 11/08/2001				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged <i>[Signature]</i> Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 75	TOTAL CLAIMS 25
				INDEPENDENT CLAIMS 16
ADDRESS 020457				
TITLE Fabrication method of semiconductor integrated circuit device				
FILING FEE RECEIVED 1970	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other <input type="checkbox"/> Credit	